

Référence

- [1] **C. Toumazou, F.J. Lidgley and D.G. Haigh** , "*Analog IC design : the current mode approach*", London,U.K, Peter Peregrinus,1990 .
- [2] **P.R. Gray and R.G. Meyer**, "*Analysis and design of analog integrated circuits*", Third Edition. John Wiley and Sons, singapore,1993.
- [3] **G. Giustolisi,G. Plamisano,G. Palumbo** , "*CMRR frequency response of CMOS operational transconductance amplifiers*", IEEE. Trans. Instrumentation and Measurements , Vol. 49,N°1,pp.137-143, February 2000.
- [4] **J. C. Voghell**, "*Réalisation de filtres analogique Gm-C configurables dans les circuits intégrés*", mémoire de maîtrise ès sciences appliquées, école polytechnique, Montréal, 2000.
- [5] **S. Yan and E. Sanchez-Sinencio**, "*Low voltage analog Circuit Design techniques: A tutorial*", IEICE. Trans. Analog Integrated Circuits and Systems", Vol. E00–A, N° 2, pp 1-17, February 2000,
- [6] **T. Itakura, T. Ueno, H. Tanimoto, A. Yasuda, R. Fujimoto, T. Arai, and H. Kokatsu**, "*A 2.7-V, 200-kHz, 49-dBm, Stopband-IIP3, Low-Noise, Fully Balanced Gm-C Filter IC*", IEEE. J. Solid-State Circuits, Vol. 34, N° 8, pp.1115-1199, August 1999.
- [7] **J.E. kardontchik**,"Introduction to the design of transconductore–capacitor filters", 1992 , Kluwer Academic Publishers, Boston (USA).
- [8] **J.M. Stevenson, and E. S´anchez-Sinencio**," *An accurate quality factor tuning scheme for IF and high-Q continuous-Time filters* ", IEEE. J. Solid-State Circuits, Vol.33,N° 12, PP.1970-1978, December , 1998.

- [9] **P.D. Walker, and M.M. Green**, "*An Approach to Fully differential circuit design without common-Mode feedback*", IEEE. Trans. Circuits and Syst. Analog and Digital Signal Processing, Vol 43, N° 11, pp.752-762, November 1996.
- [10] **R. Harjani, R. Heineke, and F. Wang**, "*An Integrated low voltage Class AB CMOS OTA*", IEEE. J. Solid State Circuits, Vol. 34, N° 2, pp 134-141, February 1999.
- [11] **B.Nauta**, "Analoge cmos filters for very high frequencies", 1993, Kluwer Academic Publishers, Nourville MA02061, USA.
- [12] **R. L. Geiger and E. Sánchez-Sinencio**, "*Active filter design using operational transconductance amplifiers: A tutorial*", IEEE. Circuits and Devices Magazine, Vol. 1, pp.20-32, March 1985.
- [13] **W. Tan, A. Karimi-Sanjaani, A. Roithmeier, and A.A. Abidi**, "*A broad-Band tunable CMOS channel-Select Filter for a low-IF Wireless Receiver Farbod Behbahani*", IEEE. J. Solid-State Circuits, Vol.35,N° 4, pp.476-488, April 2000.
- [14] **E. Sanchez-Sinencio, R.L. geiger, and H. Nevarez-Lozano**, "*Generation of continuous-Time Two integrateur Loop OTA Filter Structures*" , IEEE. Trans. Circuits Syst., Vol 35, N° 8 , pp.936-946, August 1988.